

Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-310US1	Application No. 10/070091
Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))		Applicant Gilbert Wolrich et al.	
		Filing Date	Group Art Unit

U.S. Patent Documents							
Examiner Initial	Desig. ID	Patent Number	Issue Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	5,357,617	10/18/1994	Davis et al.			
	AB	5,933,627	08/03/1999	Parady			
	AC	6,092,175	07/18/2000	Levy et al.			
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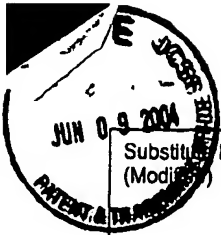
Foreign Patent Documents or Published Foreign Patent Applications								
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
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	AL							
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	AN							
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Other Documents (include Author, Title, Date, and Place of Publication)		
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



















Examiner Signature 	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

Substitute Disclosure Form (PTO-1449)

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Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-310US1	Application No. 10/070,091
Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))		Applicant Gilbert Wolrich et al.	
		Filing Date February 27, 2002	Group Art Unit 2183

Foreign Patent Documents or Published Foreign Patent Applications								
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
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	AA	WO 01/50679	07/12/2001	WIPO				
	AB	WO 01/50247	07/12/2001	WIPO				
	AC	WO 01/48619	07/05/2001	WIPO				
	AD	WO 01/48606	07/05/2001	WIPO				
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	AF	WO 01/48596	07/05/2001	WIPO				
	AG	WO 01/41530	06/14/2001	WIPO				
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	AR	EP 0 464 715	01/08/1992	Europe				
	AS	EP 0 379 709	08/01/1990	Europe				
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	AU	Agarwal et al., "April: A Processor Architecture for Multiprocessing," Proceedings of the 17 th Annual International Symposium on Computer Architecture, IEEE, pp. 104-114.
	AV	Byrd et al., "Multithread Processor Architectures," IEEE Spectrum, Vol. 32, No. 8, New York, 1 August 1995, pp. 38-46.
	AW	Chang et al., "A New Mechanism For Improving Branch Predictor Performance," IEEE, pp. 22-31 (1994).

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Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-310US1	Application No. 10/0701001
Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))		Applicant Gilbert Wolrich et al.	
		Filing Date February 27, 2002	Group Art Unit 2183

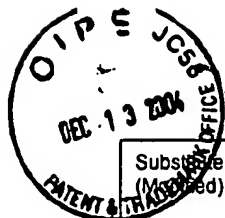
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Other Documents (include Author, Title, Date, and Place of Publication)		
Examiner Initial	Desig. ID	Document
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	AY	Farkas et al., "The multicluster architecture: reducing cycle time through partitioning," IEEE, vol. 30, December 1997, pp. 149-159.
	AZ	Fillo et al., "The M-Machine Multicomputer," IEEE Proceedings of MICRO-28, 1995, pp. 146-156.
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	ADD	Hennessy et al., "Complete Organization and Design: The Hardware/Software Interface," Morgan Kaufman Publishers, 1998, pp. 476-482.
	AEE	Hyde, R., "Overview of Memory Management," <i>Byte</i> , vol. 13, no. 4, 1998, pp. 219-225.
	AFF	Intel, "IA-64 Application Developer's Architecture Guide," Rev.1.0, May 1999, pp. 2-2, 4-29 to 4-31, 7-116 to 7-118 and c-21.
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	AHH	Litch et al., "StrongARMing Portable Communications," IEEE Micro, 1998, pp. 48-55.
	AII	Mendelson et al., "Design Alternatives of Multithreaded Architecture," <i>International Journal of Parallel Programming</i> , vol. 27, no. 3, Plenum Press, New York, USA, June 1999, pp. 161-193.
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	ANN	Turner et al., "Design of a High Performance Active Router," Internet Document, <i>Online</i> , 18 March 1999.
	AOO	Vibhatavanijt et al., "Simultaneous Multithreading-Based Routers," Proceedings of the 2000 International Conference of Parallel Processing, Toronto, Ontario, Canada, 21-24 August 2000, pp. 362-359.
	APP	Wadler, "The Concatenate Vanishes," University of Glasgow, December 1987 (revised November 1989), pp. 1-7.
	AQQ	Wazlowski et al., "PRSIM-II computer and architecture," IEEE Proceedings, Workshop on FPGAs for Custom Computing Machines, 1993.

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Substitute Form PTO-1449 (Mandatory)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-310US1	Application No. 10/070,091
Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))		Applicant Gilbert Wolrich et al.	
		Filing Date February 27, 2002	Group Art Unit

U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
<i>h</i>	AA	09/473,571	12/28/1999	Wolrich et al.			
<i>h</i>	AB	09/475,614	12/30/1999	Wolrich et al.			
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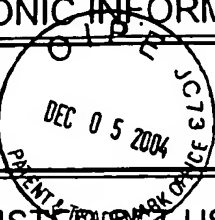
Foreign Patent Documents or Published Foreign Patent Applications								
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
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<i>h</i>	AH	WO 01/16697	03/2001	WIPO				
<i>h</i>	AI	WO 01/16698	03/2001	WIPO				
<i>h</i>	AJ	WO 01/16703	03/2001	WIPO				
<i>h</i>	AK	WO 01/16713	03/2001	WIPO				
<i>h</i>	AL	WO 01/16714	03/2001	WIPO				
<i>h</i>	AM	WO 01/16715	03/2001	WIPO				
<i>h</i>	AN	WO 01/16716	03/2001	WIPO				
<i>h</i>	AO	WO 01/16722	03/2001	WIPO				
<i>h</i>	AP	WO 01/16758	03/2001	WIPO				
<i>h</i>	AQ	WO 01/18646	03/2001	WIPO				
	AR							

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Examiner Initial	Desig. ID	Document
<i>h</i>	AS	Paver et al., "Register Locking in Asynchronous Processor Computer Design: VLSI Processors," ICCD '92 Proceedings, IEEE 1992 International Conference, 1992, pp. 351-355.
<i>h</i>	AT	Waldspurger et al., "Register Relocation: Flexible Contents for Multithreading," Proceedings of the 20th Annual International Symposium on Computer Architecture, 1993, pp. 120-130.
	AU	

Examiner Signature <i>[Signature]</i>	Date Considered <i>[Signature]</i>
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ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18
Stylesheet Version v18.0



Title of Invention

REGISTERED SET USED IN MULTITHREADED PARALLEL
PROCESSOR ARCHITECTURE

Application Number: 10/070091

Confirmation Number: 7309

First Named Applicant: Gilbert Wolrich

Attorney Docket Number: 10559-310US1

Search string: (3373408 or 3478322 or 3792441 or 3913074 or 3940745 or
4130890 or 4392758 or 4400770 or 4514807 or 4523272 or
4724521 or 4745544 or 4777587 or 4866664 or 5073864 or
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20020053017 or 20020056037 or 20030041228 or 20030145159 or
20030191866 or 20040039895 or 20040054880 or 20040071152 or
20040073728 or 20040073778 or 20040098496 or 20040109369 or
20040205747).pn.



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US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
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4	2	3478322	1969-11-11	Evans			
4	3	3792441	1974-02-12	Wymore et al.			
4	4	3913074	1975-10-14	Homberg et al.			
4	5	3940745	1976-02-24	Sajeva			
4	6	4130890	1978-12-19	Adam			
4	7	4392758	1983-07-12	Bowles et al.			
4	8	4400770	1983-08-23	Chan et al.			
4	9	4514807	1985-04-30	Nogi			
4	10	4523272	1985-06-11	Fukunaga et al.			
4	11	4724521	1988-02-09	Carron et al.			
4	12	4745544	1988-05-17	Renner et al.			

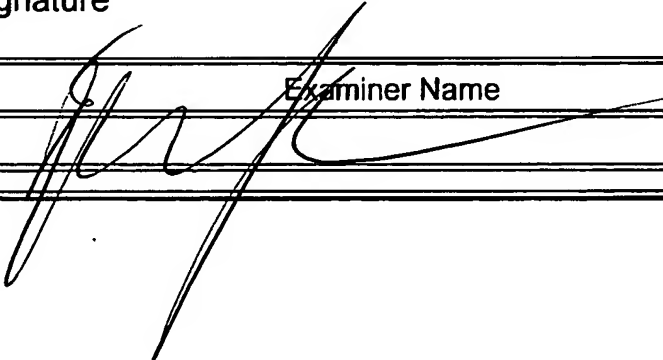
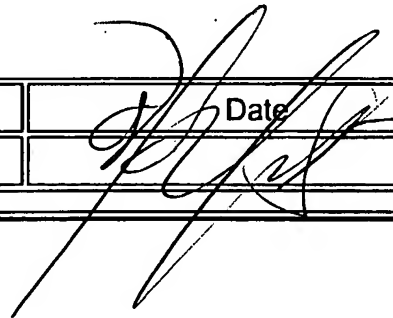
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US Published Applications

Note: Applicant is not required to submit a paper copy of cited US Published Applications

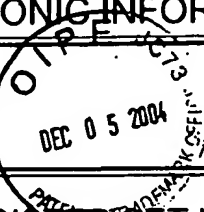
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9	1	20020053017	2002-05-02	Adiletta et al.			
9	2	20020056037	2002-05-09	Wolrich et al.			
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Signature

	Examiner Name		Date

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18
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Title of Invention

REGISTER SET USED IN MULTITHREADED PARALLEL
PROCESSOR ARCHITECTURE

Application Number: 10/070091

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First Named Applicant: Gilbert Wolrich

Attorney Docket Number: 10559-310US1

Search string: (5568617 or 5574922 or 5574939 or 5592622 or 5606676 or
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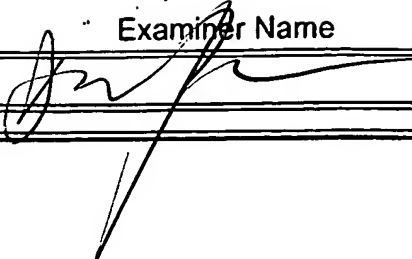

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Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
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Signature

Examiner Name 	Date 
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First Named Applicant: Gilbert Wolrich

Attorney Docket Number: 10559-310US1

Search string: (5937187 or 5938736 or 5940612 or 5940866 or 5946487 or
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US Patent Documents

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init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
U	1	5937187	1999-08-10	Kosche et al.			
U	2	5938736	1999-08-17	Muller et al.			
U	3	5940612	1999-08-17	Brady et al.			
U	4	5940866	1999-08-17	Chisholm et al.			
U	5	5946487	1999-08-31	Dangelo			
U	6	5948081	1999-09-07	Foster			
U	7	5951679	1999-09-14	Anderson et al.			
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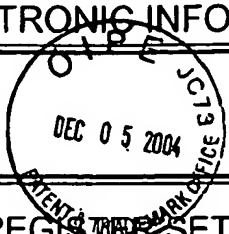
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Title of Invention

REGISTER SET USED IN MULTITHREADED PARALLEL
PROCESSOR ARCHITECTURE

Application Number: 10/070091

Confirmation Number: 7309

First Named Applicant: Gilbert Wolrich

Attorney Docket Number: 10559-310US1

Search string: (6223238 or 6223279 or 6247025 or 6256713 or 6272616 or
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6324624 or 6345334 or 6347344 or 6351808 or 6356962 or
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